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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

## U.S. UTILITY Patent Application

O.I.P.E. PATENT DATE  
 SCANNED D<sup>2</sup> Q.A. TC

APPLICATION NO. 09/639613	CONT/PRIOR	CLASS <del>705</del> 316	SUBCLASS 4	ART UNIT <del>2181</del> 2825	EXAMINER TAT
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## APPLICANTS

**TITLE**

# Electrical rules checker system and method using tri-state logic for electrical rule checks

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PTO-2040  
12/99

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
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